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PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: : PERVEZ H. SAGARWALA ET AL
Serial No. : 09/800,039
Filed : March 6, 2001
For : CMOS INTEGRATED CIRCUIT DEVICE WITH LDD N-CHANNEL TRANSISTOR AND NON-LDD P-CHANNEL TRANSISTOR
Group No. : 2822
Examiner : M.V. Prenty

MAIL STOP APPEAL BRIEF - PATENTS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

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The undersigned hereby certifies that the following documents:

1. Postcard receipt;
2. Check in the amount of \$320.00 for the Appeal Brief filing fee;
3. Check in the amount of \$110.00 for the Extension of Time filing fee;
4. Appeal Brief (in triplicate);
5. Supplemental Amendment and Response to Final Office Action; and,
6. Petition for Extension of Time (1 mo.) (in duplicate)

relating to the above application, were deposited as "First Class Mail" with the United States Postal Service, addressed to: MAIL STOP APPEAL BRIEF - PATENTS, Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313-1450, on May 27, 2003.

Date: 5/27/03

Date: 5-27-03

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